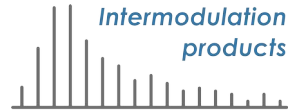


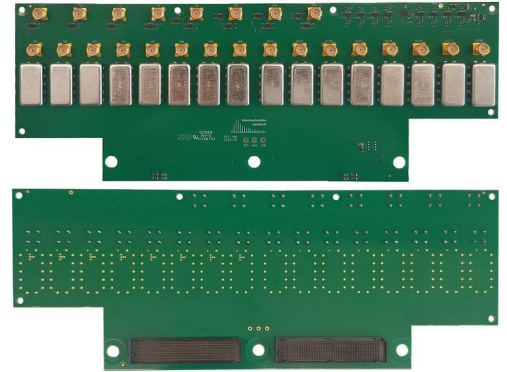
# Product brief – VFE-100

Latest update: 2023-11-27



## Front-end / mezzanine board for Xilinx RFSoc

- Single-ended 50 ohm access to all ADC and DAC channels
- Wide-band balun transformers.
- Bias tee and programmable DC bias on all output (set from FPGA) for IQ mixer balancing and other applications.
- Pair-wise length matched traces.
- Digital inputs and outputs for markers / triggers.
- Replaces HW-RFMC-XM500.



### RF inputs

# ports	8 (SMA)
Impedance	50 $\Omega$
Coupling	AC
Bandwidth	10 MHz to 4 GHz

### RF outputs

# ports	8 (SMA)
Impedance	50 $\Omega$
Coupling	AC + bias
Bandwidth	10 MHz to 4 GHz
Bias	Built-in bias tee for DC offset, 16 bit DAC, $\pm 1.25$ V

### Digital markers / triggers

# input ports	4 (SMA)
Input impedance	10 k $\Omega$
# output ports	4 (SMA)
Output impedance	50 $\Omega$
Output voltage	3.3 V

### General

Size, weight	250 mm x 94 mm, 0.2 kg
Connectors	24x SMA, 2x LPAM-40 (RFMC 1.0)
Compatible with	ZCU111
Country of origin	Sweden
Classifications	RoHS, HS 84733020